HA-8-3 Board Resources

Memory

        The HA-8-3 does not use any part of the H8 memory space

I/O Ports

        All I/O ports are assigned using GAL U5

066Q	Soft Reset (any write to this port resets the VDP)
270-271Q	Video Display Processor (VDP)
272-273Q	Programmable Sound Generator (PSG)

        Default Assignments, Jumper JP14 Removed

364-365Q	Analog to Digital Converter (ADC)
366-367Q	Arithmetic Processing Unit (APU)

        Alternate Assignments, Jumper JP14 Installed

274-275Q	Analog to Digital Converter (ADC)
276-277Q	Arithmetic Processing Unit (APU)

Interrupts

        By default, interrupts are not used on the HA-8-3

        The VDP and APU are capable of generating interrupts

        Jumper selected for INT 3,4,5 or 6 using P12 and P13