HA-8-3 Jumper Settings

			LEFT/BOTTOM/ON	RIGHT/TOP/OFF	Default	
1	1X3	RD_L	Latched	Normal	LEFT	
2	1X3	WR_L	Latched	Normal	LEFT	
3	1X3	9918/9928 Pin 38	Pin 38=VDPCLK (9918)	Pin 38=Pr (9928)	RIGHT	LEFT for 9918, RIGHT for 9928
4	1X3	/IORQF	Z80 v4 IORQ	"Fake" IORQ=IORD+IOWR	BOTTOM	BOTTOM for Z80v4, TOP for others
5	1X3	9928/9918 Pin 35	Pin 35=Pb (9928)	Pin 35=GND (9918)	LEFT	LEFT for 9928; RIGHT if no ext video in for 9918; not installed if using 9918 external video
6	1X2	/APU_WAIT	APU Waits On	No APU Waits	ON	
7	1X3	END/SVREQ	APU Int on END	APU Int on SVREQ	1-2	See P12 for APU interrupt jumper
8	1X2	/IO_WAIT	IO Waits On	No I/O Waits	OFF	If using I/O Wait States, adjust RV3 for ~700ns pulse width at JP8
9	1X2	+18v/+12v	Bypass board +12v	Use on-board regulator	OFF	CAUTION! Do not install unless the H8 bus has been modified for PC-style power supply
10	1X2	-18v/-12v	Bypass board -12v	Use on-board regulator	OFF	CAUTION! Do not install unless the H8 bus has been modified for PC-style power supply
11	1X3	MUX Enable	AD7503 - Enable=GRD	AD7501 - Enable=+5v	TOP	Set according to MUX IC being used
12	1X2	/AD_WAIT	ADC Waits On	No ADC Waits	ON	
13	1X2	/AD_BUSY	ADC Busy Waits On	No ADC Busy Waits	ON	
14	1X2	SEL_IOPORT	Use Original HA-8-3 Ports	Use triple-disk ports	OFF	OFF: uses triple disk compatible ports, ON: uses original HA-8-3 addresses
15	1X3	OSC2/Clk	Use OSC2 for PSG clock	Use JP21 for PSG clock	(*)	See PSG clock selection matrix
16						
17	1X2	/PSGWAIT	PSG Waits On	No PSG Waits	ON	
18						
19	1X2	+8v/+5v	Bypass Primary +5v	Use on-board regulator	OFF	CAUTION! Do not install unless the H8 bus has been modified for PC-style power supply
20	1X2	+8v/+5v	Bypass Secondary +5v	Use on-board regulator	OFF	CAUTION! Do not install unless the H8 bus has been modified for PC-style power supply
21	1X3	OSC1/9918	Use OSC1 (APU) for PSG clock	Use 9918 for PSG clock	(*)	See PSG clock selection matrix
22						
23	1X2	BUFFER-ON/OFF	Enable bus buffers	Buffers OFF for DEBUG	ON	Off only for debug testing

Default Configuration:
TMS9928 VDP
No AM9511 APU installed
AD7501 MUX IC
Z80 v4 CPU board
No general I/O wait states, or wait states produced by other board